

STORE MATRIX PA1/510 SERIES

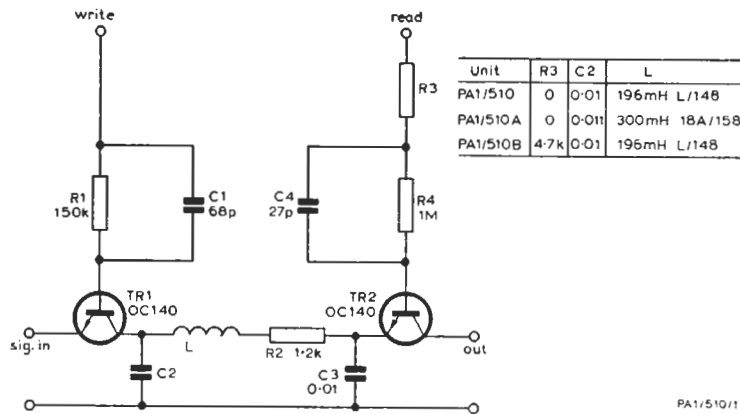


Fig. 1 Circuit of a Single Store

The PA1/510 comprises 64 video stores constructed on four identical printed wiring cards. Each store is fed with a picture-information signal and two sets of sampling pulses.

The circuit of a single store is given in Fig. 1. A positive-going write pulse connects the input of the store to a low-pass filter and a positive-going read pulse connects the filter to the output of the store. Networks C1, R1 and C4, R4 shape the sampling pulses fed to the symmetrical npn transistors. The filter passes signals up to about half the write-pulse frequency.

The stores on a PA1/510 are arranged in sixteen rows of four columns each as shown in Fig. 2. The signal inputs and outputs of all the sixteen stores in each column (sometimes called an element) are connected in parallel. The write and read inputs of the four stores in each row are connected in parallel. These connections are given in Tables 1 and 2.

TABLE 1

Element	Signal in	Signal out
n + 1	B12	D12
n + 2	B13	D13
n + 3	B14	D14
n + 4	B15	D15

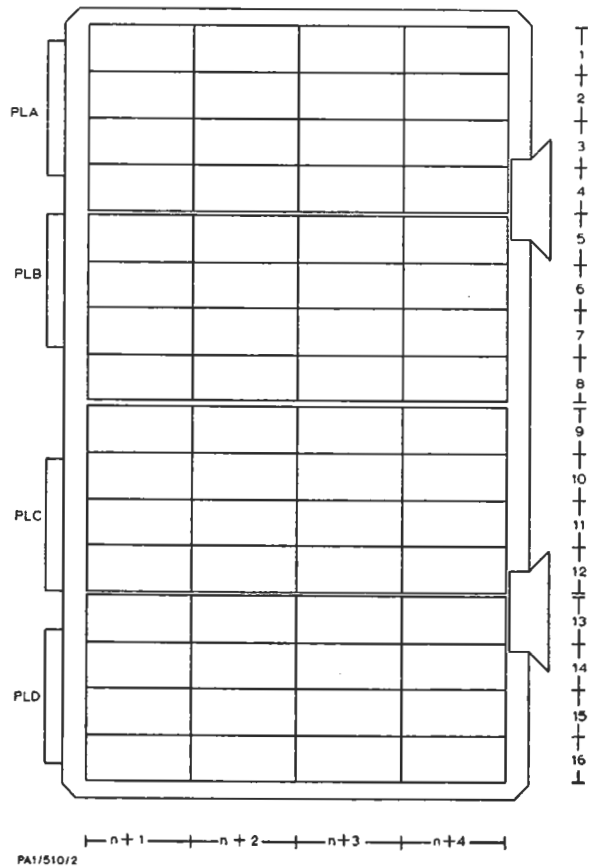


Fig. 2 Arrangement of Stores in PA1/510

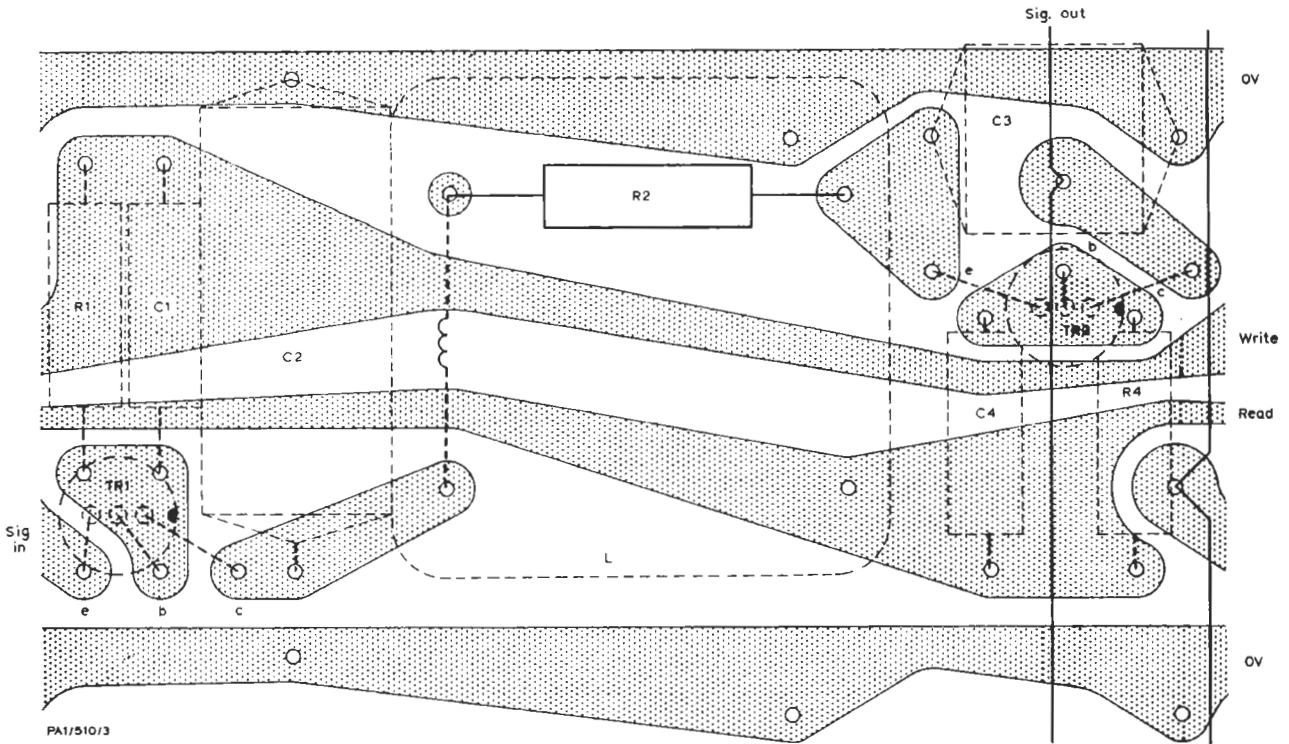


Fig. 3 Layout of a Single Store on the PA1/510

TABLE 2

Store	Write	Read
1	A3	A4
2	A5	A6
3	A7	A8
4	A9	A10
5	B3	B4
6	B5	B6
7	B7	B8
8	B9	B10

Store	Write	Read
9	C3	C4
10	C5	C6
11	C7	C8
12	C9	C10
13	D3	D4
14	D5	D6
15	D7	D8
16	D9	D10

The PA1/510 is constructed on an Elliot Minilog mounting chassis, the dimensions of which are

17 inches by 10 inches by 1 1/4 inches.

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