

PULSE CIRCUITS PANEL PA1/518 SERIES

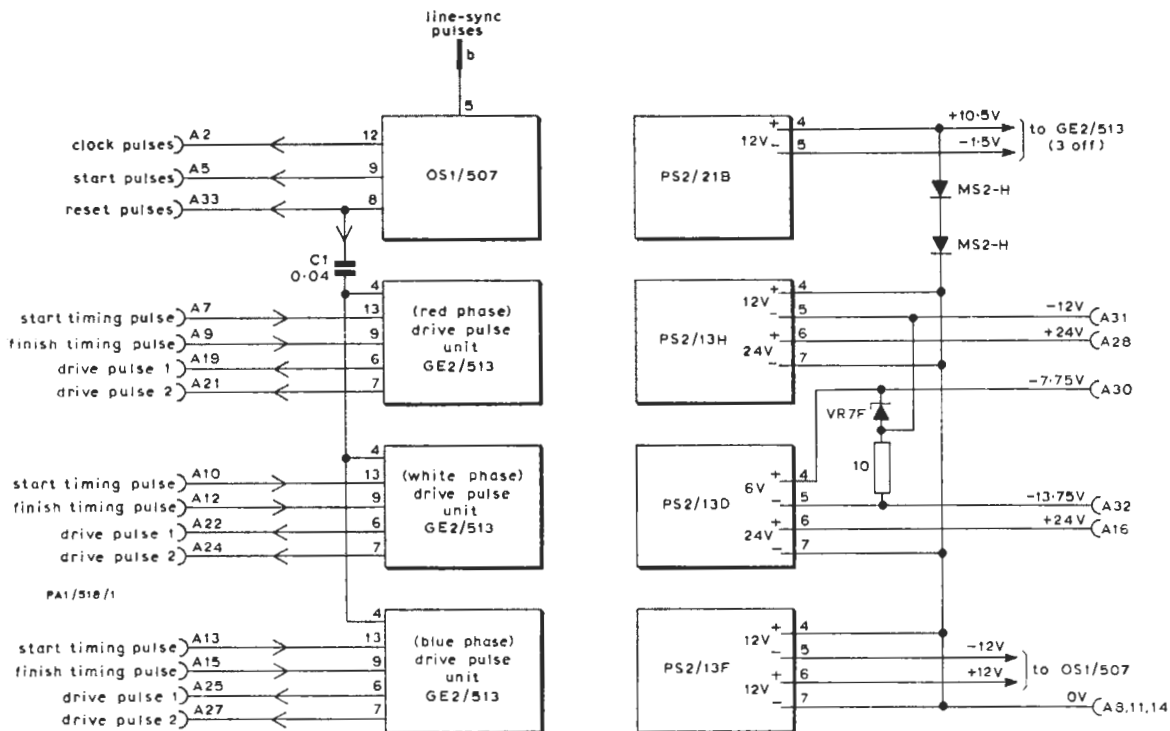


Fig. 1 Circuit of the PA1/518

Introduction

The PA1/518 accepts a line-sync pulse input, three pairs of timing pulse inputs and a mains input; it produces the following outputs:

- (a) Positive-going clock pulses at approximately twice the highest video frequency for the line standard used.
- (b) Negative-going start pulses at line frequency.
- (c) Positive-going reset pulses. These are inverted start pulses.
- (d) Three pairs of outputs each giving 16 negative-going drive pulses in each line-period. Pulses from these six outputs occur in rotation.
- (e) Seven d.c. outputs:
 - + 24 volts (two)
 - + 12 volts
 - 7.75 volts
 - 12 volts (two)
 - 13.75 volts

The PA1/518 comprises the following units mounted in a panel PN3/23:

- Power Supplier PS2/21B
- Power Supplier PS2/13H
- Power Supplier PS2/13D

Power Supplier PS2/13F

Pulsed Oscillator (see Table 1)

Drive Pulse Unit GE2/513 (three)

Table 1 shows the type of pulsed oscillator used in each pulse circuits panel in the series.

TABLE 1

PA1/518A	OS1/501A
PA1/518B	OS1/501B
PA1/518C	OS1/507A
PA1/518D	OS1/507B

Description

Fig. 1 shows the circuit diagram of the PA1/518. The timing pulses are derived externally from the clock pulses in a 36-stage shift register.

Test Procedure

The PA1/518 is tested as part of its parent unit.
MJR 9/68