

SECTION 5

PERIODIC TESTER TE1/504

Introduction

The TE1/504, as used in the Television Automatic Monitor Major MN2M/506 (see Instruction V.11), produces a test fault as a means of periodically checking the Monitor.

The TE1/504 is constructed on a CH1/12B chassis with index peg positions 18 and 29.

Circuit Description

The circuit of the Tester is given in Fig. 5.1. A mains-driven timer, comprising a synchronous motor driving a cam, makes a contact for about 5 seconds every 15 minutes. While the contact is made relay RLC is operated which makes the Reset Intergrator contact RLC-2. Contact RLC-1 discharges capacitor C1.

At the end of the 5-second period relay RLC

releases and relay RLB operates via contact RLC-1 and capacitor C1. Contact RLB-2 holds-on relay RLB via contact RLA-1 (relay RLA is operated via an external circuit). Contact RLB-2 also discharges capacitor C1 and applies +12 volts to the Reset Indicator output.

Contact RLB-1 closes a test circuit. If the Monitor is operating correctly, this results in +12 volts being applied to the inputs from four detectors on pins 11 to 14. Application of +12 volts to all four inputs releases relay RLA. Contact RLA-1 breaks and releases relay RLB. Capacitor C1 delays the release of relay RLB.

Test Procedure

The TE1/504 is tested as part of an Automatic Monitor Major: see Instruction V.11.

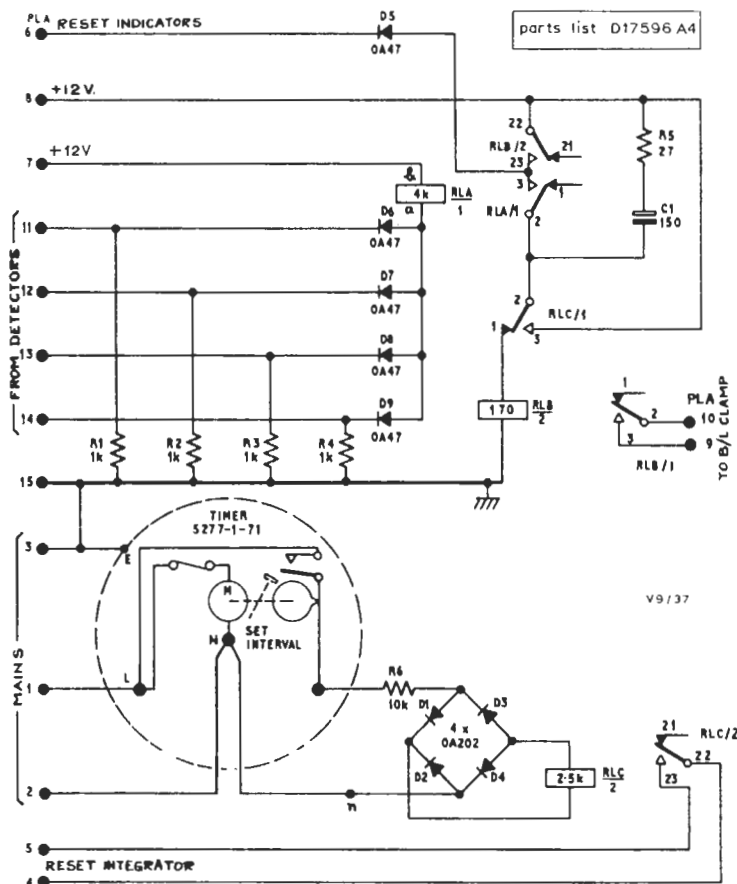


Fig. 5.1 Circuit of the TE1/504