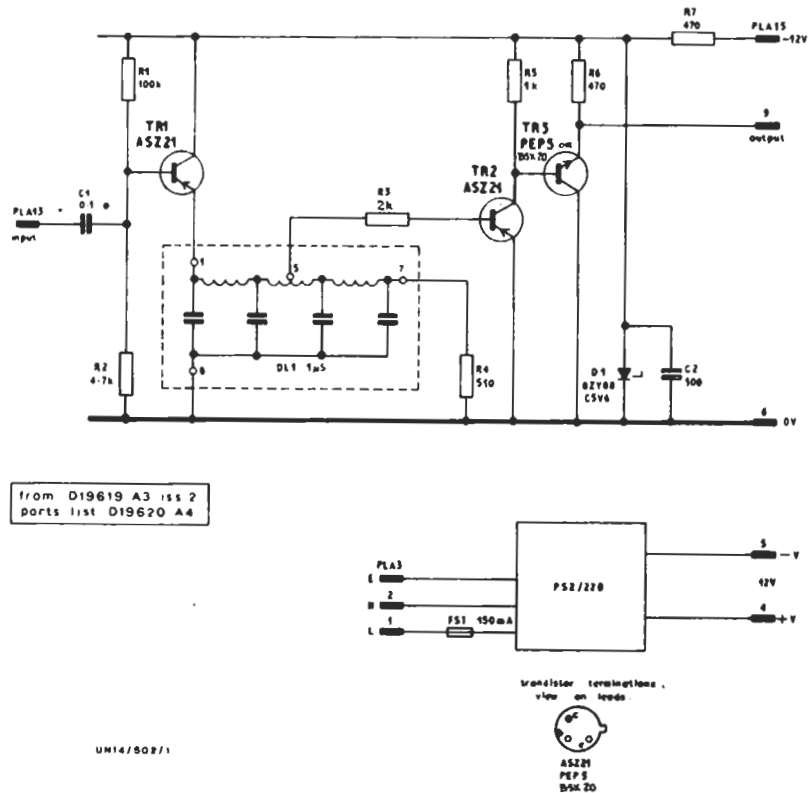


**BURST GATE DELAY UNIT UN14/502**



**Fig. 1** Circuit of the Burst-gate Delay Unit UN14/502

The UN14/502 accepts an input of burst gate pulses and produces an output of these pulses inverted, amplified and delayed by 600 ns. The unit, which also contains a power supplier PS2/22B, is constructed on a chassis CH1/12A with index-peg positions 9 and 31.

The circuit of the UN14/502 is given in Fig. 1. The amplitude of the output pulses is limited by means of zener diode D1 to about 5 volts p-p.

**Typical Associated Equipment**

1. Colour Subcarrier Phase Comparators EP5/505.6 Series.

MJR 4/69

AIB 1/72