

405-625 STANDARDS DETECTOR UN20/514

The UN20/514 accepts two composite video signals, each of which may be on either the 405-line standard or the 625-line standard; it operates a relay for each input which is on a preselected standard. Standard selection is made by means of a soldered wire link for each of the two inputs. The detector operates at input levels down to -10 dB relative to 1 volt p-p.

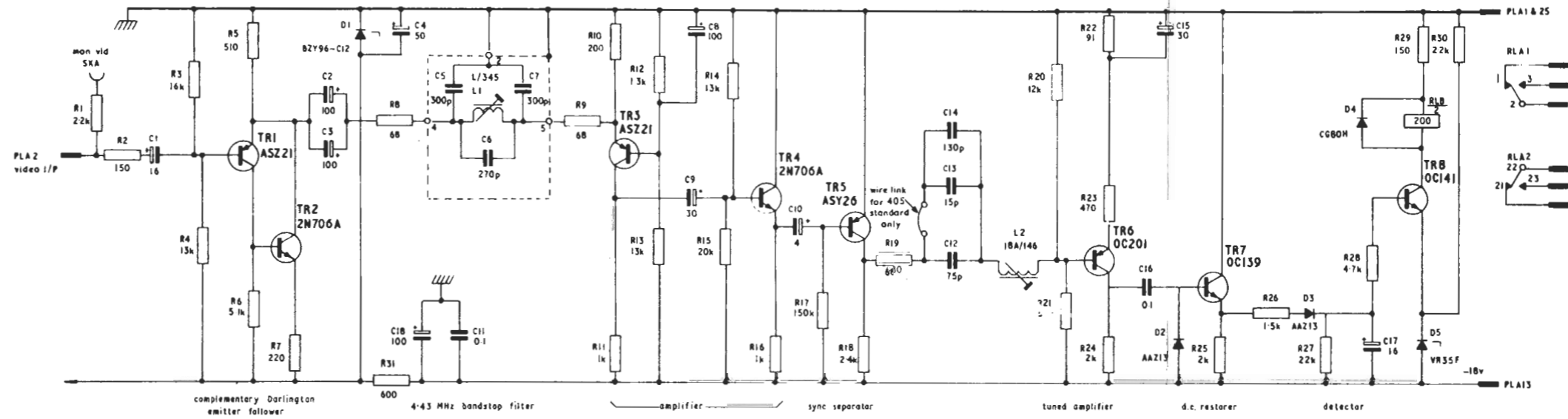
The UN20/514 is constructed on a CH1/43B (A-sized) chassis with index-peg positions 1, 2 and 4.

The circuit of the UN20/514, given in Fig. 1 on

page 3, comprises two similar detector circuits. The input signals are fed to simple sync separators, transistors TR5 and TR15, via chrominance traps. Positive-going sync pulses from the collectors of these transistors are fed to circuits tuned to line-frequency. The resulting sinusoidal signals are d.c. restored and detected to operate relays. Zener diodes are used to bias transistors TR8 and TR18 to prevent the relays operating with low-level or wrong line-standard input signals.

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See page 3 for Fig. 1



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parts list D21485 A4

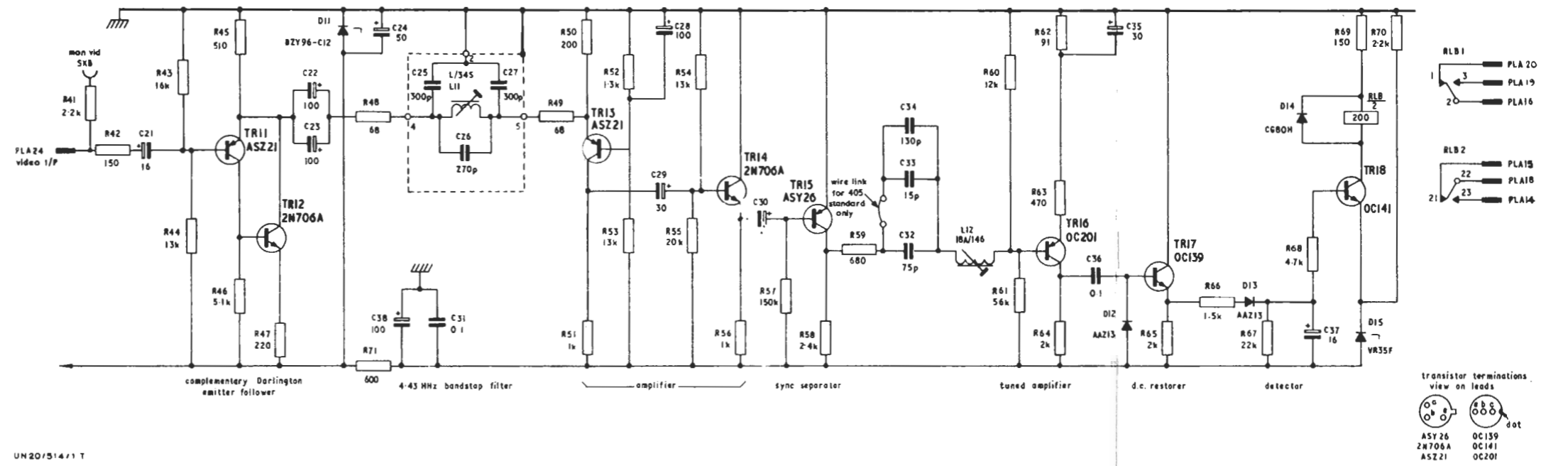


Fig. 1 Circuit of 405-625 Standards Detector UN20/514