DESIGNS DEPARTMENT HANDBOOK

No. 3.250(80)

AM5/33 Mixer Amplifier

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 D 49837 A2

 Parts List
 D 49838 A4

 P.B. Comp. Loc.
 D 49843 A2

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1. INTRODUCTION

The AM5/33 contains two identical circuits each of which consists of three differential input amplifiers whose outputs are mixed and taken to a non-inverting amplifier output stage.

Input 1, which is designed to be fed from a microphone channel has unity gain but inputs 2 and 3, which are fed with Echo Return signal have an overall loss of -15 dB.

The amplifier is contained on a 3U BMM unsupported card. The card is designed for use in the PA6/87 module of the EP10/18 Radio Continuity Suite.

2. PERFORMANCE SPECIFICATION

2.1 Power Supplies

+12 volt at 60 mA.

2.2 A.C. Performance

Maximum output	O dB		
Gain	Input 1 0 dB	Input 2 -15 dB	Input 3 -15 dB
Input impedance	20 k Ω		
Frequency response	40 Hz-15 kH	Iz <u>+</u> 0.03 dB v	v.r.t. 1 kHz
Noise	-80 dB ₄		
Distortion	Less than 0	.1% T.H.D. a	at O dB

3. CIRCUIT DESCRIPTION

The AM5/33 carries two identical circuits. In this circuit description only one half will be referred to.

IC1b, c and d form three differential input amplifiers. Amplifier 1 has unity gain and amplifiers 2 and 3 have a gain of -15 dB. The outputs of these amplifiers are mixed and fed to IC2a, a non-inverting

D.D. Handbook No. 3.250(80) Sheet 1 of 2 sheets output amplifier with a gain of 12 dB.

4. MAINTENANCE

If a fault occurs the d.c. conditions should be checked first. The supply currents should not exceed those given in 2.1 The output pin of each integrated circuit amplifier should be at OV ± 20 mV.



